

Consultation on Co-packaged Optical PAM4 Configuration Scheme

This white paper provides a technical deep dive into the critical AMS circuit blocks of high-speed SerDes tailored for next-generation optical transceivers, with a focus on co-packaged...

This paper explains how 224 Gbps PAM4 systems differ from previous generations in terms of interconnects, what technologies and methodologies enable 224 Gbps PAM4 interconnects, and ...

These architectural examples are leveraged to extend the interoperable ecosystem to 224G and enable a meter of backplane with host and daughter cards, for "line card to line card" or "AI/ML architecture" ...

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This talk presents a 112-Gb/s four-level pulse amplitude modulation (PAM-4) extra-short-reach (XSR) transceiver (TRX) for next-generation co-packaged optics application.

DSP Interaction Optimal pre-FIR configuration is strictly dependent on the Rx DSP (FFE vs. MLSE).

At OFC 2024, Intel showcased a 4 Tbit/s (bi-directional) Optical Compute Interconnect (OCI) chiplet co-packaged with a concept Xeon CPU, running error-free data over a single-mode ...

Based on semiconductor indium phosphide, efficient at absorbing and emitting light and allows integration of electronic and optical components; supports both EAM and MZM

In this work, we show how MRMs can be efficiently used to implement phase-constant amplitude modulators and form the building blocks of a transmitter for offset-QAM-16, which has ...

Learn how co-packaged optics boost signal integrity and power efficiency in 51.2 Tb/s switches. Supported regionally by McKinsey Electronics.

We simulate and evaluate the performance of our proposed MRM-based coherent CPO (C2PO) transmitters using a foundry-provided commercial silicon photonics process, demonstrating ...

Our final example is an interoperability ecosystem of our co-packaged copper and optical solutions. Whether you're going to the front panel or the backplane, with copper or optics, there are ...



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